Notice of References Cited

Application/Control No. 10/691,966	Applicant(s)/Patent Under Reexamination ABDEL-HAFEZ ET AL.	
Examiner	. Art Unit	
John P. Trimmings	2133	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-6,075,418	06-2000	Kingsley et al.	331/57
	В	US-2002/0120896	08-2002	Wang et al.	714/731
	С	US-2002/0138801	09-2002	Wang et al.	714/729
	D	US-2004/0078741	04-2004	Morton, Gary	714/726
	E	US-			
	F	US-			
	G	US-			
	Н	US-			
		US-			
	J	US-			
-	К	US-			
	L	US-			
	М	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N				•	
	0					
	Р					
	Q		·	,		
	R					
	s					
	Т					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	υ	Lattice Semiconductor, "ORCA(r) Series 4 FPGAs", January, 2003, Data Sheet by Lattice Semiconductor, see internet address attached, accessed 9/1/2005.
	٧	
	8	
	x	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.